

THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of: § **Patent No.:** 6908822
RENDON, MICHAEL J., et al. §
Application No.: 10/662832 § **Issue Date:** 6/21/2005
Filed: 9/15/2003 § **Examiner:** Edward Joseph Wojciechowicz
Docket No.: SC12866TP § **Group Art Unit:** 2815
§
Title: SEMICONDUCTOR DEVICE HAVING AN INSULATING LAYER AND METHOD
FOR FORMING

Certificate of Submission

I hereby certify that this correspondence is being submitted to
the U.S.P.T.O., Alexandria, VA.

Addressed per 37 C.F.R.§ 1.1(a) and deposited with the
United States Postal Service with sufficient postage as first
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Submitted electronically via EFS in accordance with
"Legal Framework for EFS Web".

Date of Submission

Signature

Pat Thomas

Printed Name of Person Signing Certificate

Commissioner for Patents
Alexandria, VA 22313

SUBMISSION OF CERTIFICATE OF CORRECTION

Dear Commissioner:

Enclosed is a Certificate of Correction listing error(s) in the subject patent. Please enter
these corrections. Since the errors appear to be on the part of the United States Patent Office,
there should be no charge.

Respectfully submitted,

9/08/06
Date


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CERTIFICATE OF CORRECTION

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PATENT NO.: 6908822
APPLICATION NO: 10/662832
DATE: 6/21/2005
INVENTOR(S): RENDON, MICHAEL J.

It is certified that error appears in the above-identified patent and that said Letters Patent are hereby corrected as shown below:

In Column 10, Line 10, Claim No. 15:

Insert a carriage return after “comprising:”

In Column 10, Line 53, Claim No. 27:

Delete “die”

MAILING ADDRESS OF SENDER (Please do not use customer number below)

Freescale Semiconductor, Inc.

Law Department

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